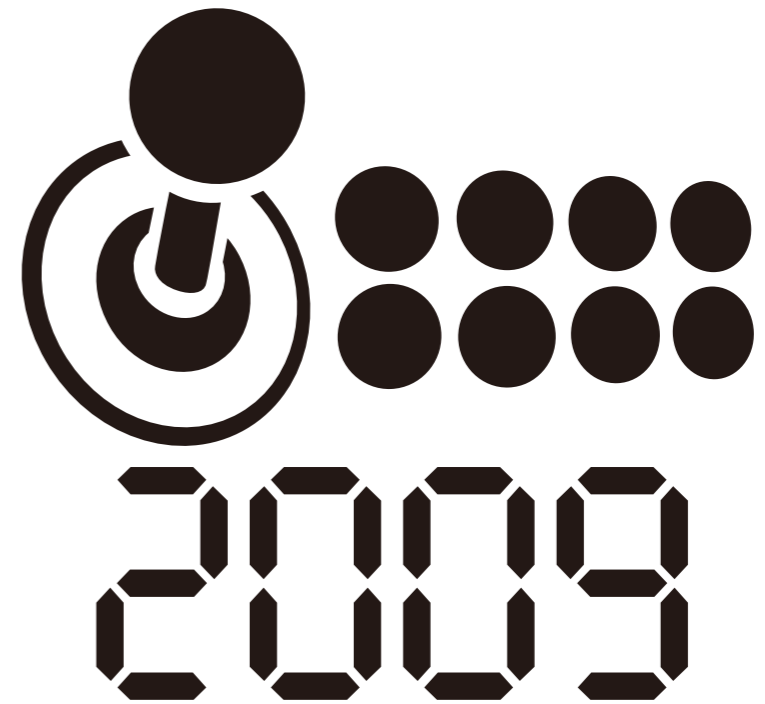


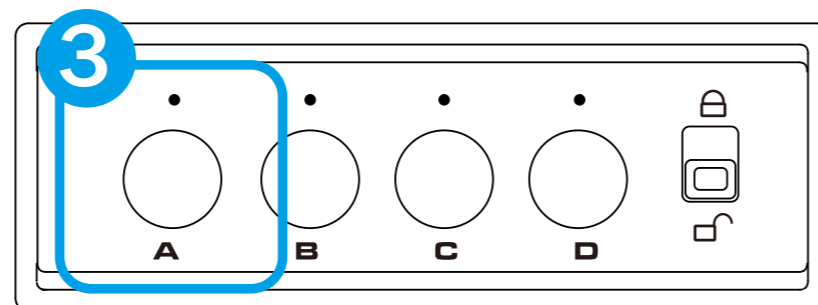
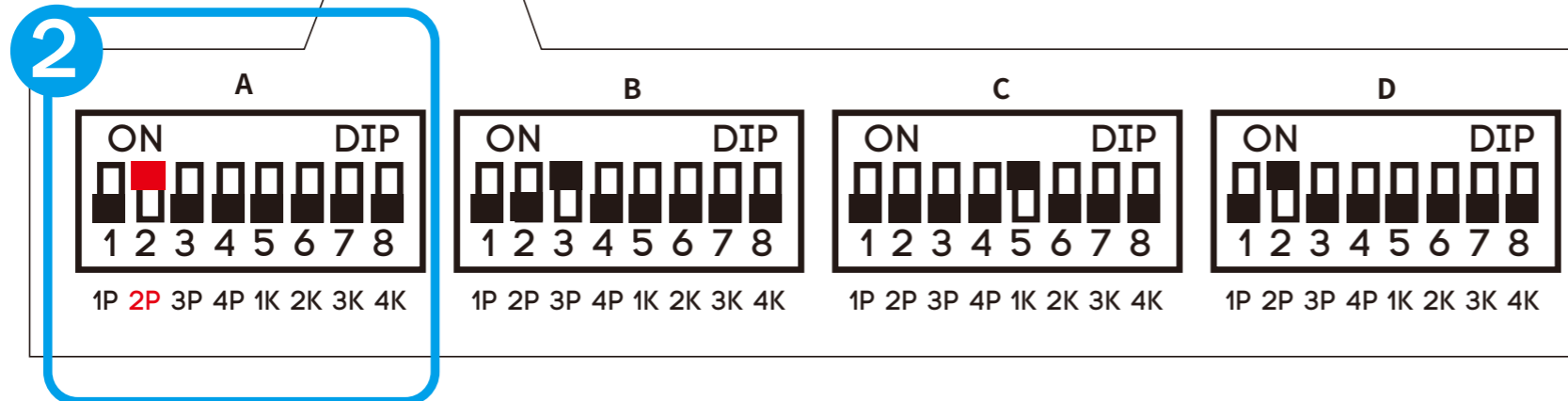
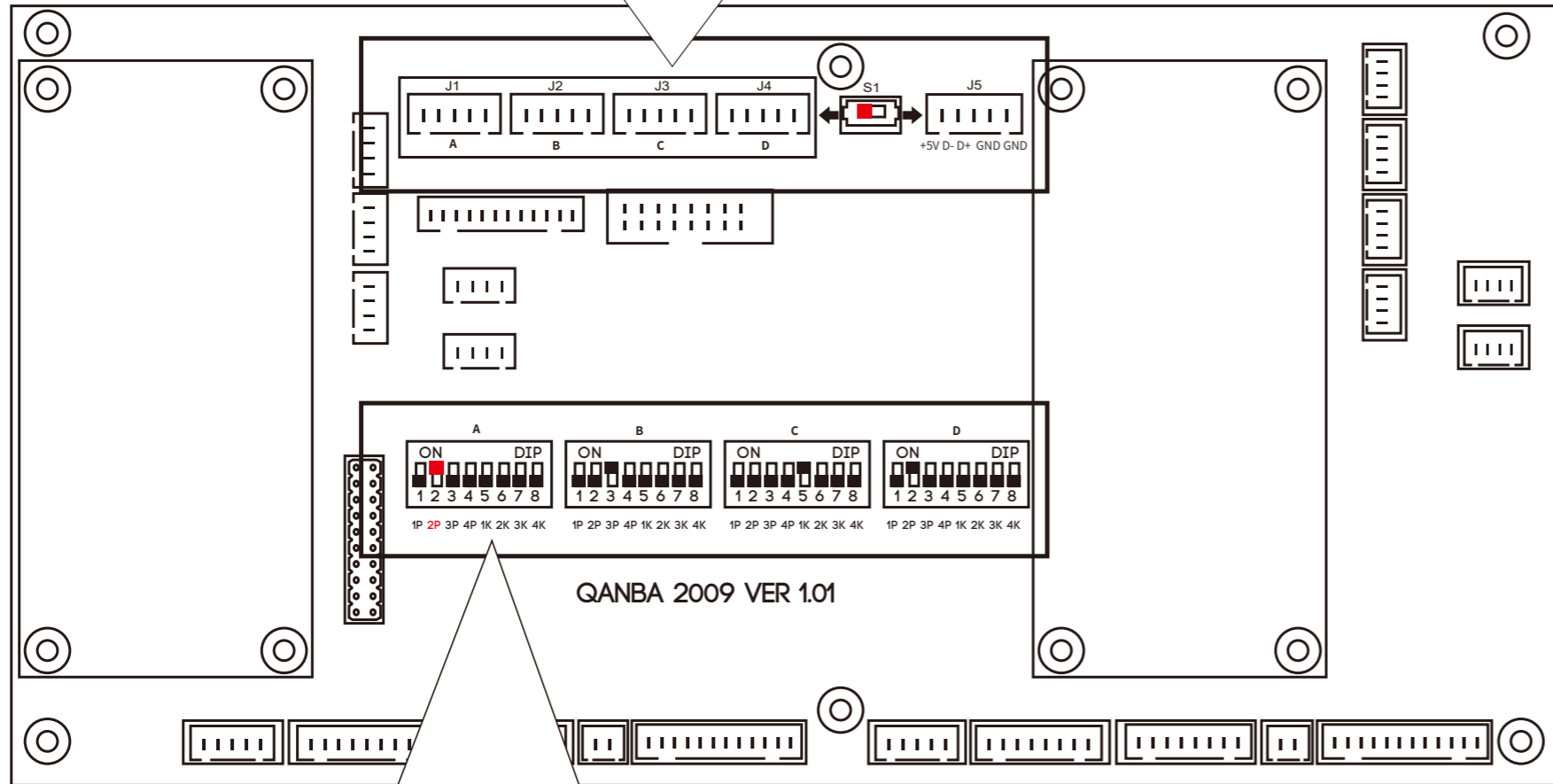
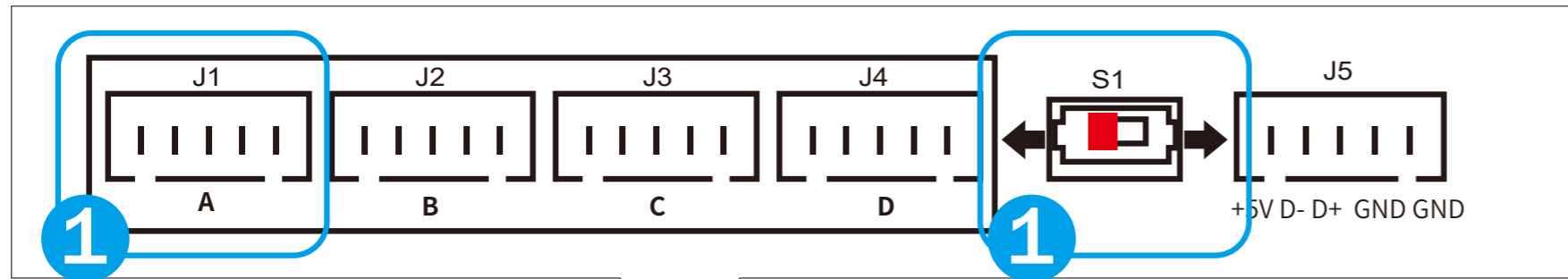
# QANBA



Qanba 2009 PCB 가

Ver1.1





1. DIP S1 " "
- J1-J4 USB
2. A J1 USB A
- " " " " " "
- 2 " " " " " "
3. A "2P" Di nput
- 가 " " " " " "
- \* DIP A, B, C, D Di nput, PC-Xi nput, Androi d
- Qanba PCB Di nput, PC-Xi nput, Androi d PCB
- PCB PCB

